Type	Hits	Search Text	DBs	Time Stamp	Comments
1 BRS	26	measur$6 with integrated with circuit$1 with processor$1 with power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:35	
2 BRS	0	measur$6 with integrated with circuit$1 with processor$1 with power and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:21	
3 BRS	2	measur$6 with integrated with circuit$1 with processor$1 with power and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:23	
4 BRS	2	measur$6 with integrated with circuit$1 and processor$1 with power and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and calculat$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:48	
5 BRS	0	measur$6 with integrated with circuit$1 with processor$1 with power and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and calculat$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:24	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	4	measur$6 with integrated with circuit$1 with power and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and calculat$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:49	
7	BRS	81	measur$6 with (CPU or processor) with current with consumption	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:29	
8	BRS	1	measur$6 with (CPU or processor) with current with consumption and power and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and calculat$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:32	
9	BRS	6	measur$6 with (CPU or processor) with current with (consumption or power) and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and calculat$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:33	

Type	Hits	Search Text	DBs	Time Stamp	Comments
10 BRS	24	measur$6 with (CPU or processor) with current with (consumption or power) and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:34	
11 BRS	0	measur$6 with (CPU or processor) with (consumption or power) with demand and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:37	
12 BRS	0	measur$6 with (CPU or processor) with (consumption or power) and oscillat$3 with ammeter$1 and (calibrat$3 or adjust$3 or compens$3) with current$1 with voltage$2 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:35	
13 BRS	1	measur$6 with (CPU or processor) with (consumption or power) and oscillat$3 with ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:51	

Type	Hits	Search Text	DBs	Time Stamp	Comments
14 BRS	0	measur$6 with (CPU or processor) with (consumption or power) and (calibrat$3 or adjust$3 or compens$3) with voltage$1 with oscillator$1 with ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:50	
15 BRS	2	measur$6 with (CPU or processor$1) with (consumption or power) and (calibrat$3 or compens$3) with voltage$1 with oscillator$1 and ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:49	
16 BRS	12	resist$4 with ladder with voltage with taps with select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:44	
17 BRS	0	(calibrat$3 or adjust$3 or compensat$3) with oscillat$3 and resist$4 with ladder with voltage with taps with select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:54	
18 BRS	2	measur$6 with integrated with circuit$1 and processor$1 with power and (calibrat$3 or adjust$3 or compensat$3) with current$1 with voltage$2 and calculat$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:48	

Type	Hits	Search Text	DBs	Time Stamp	Comments
19 BRS	2	measur$6 with (CPU or processor$1) with (consumption or power) and (calibrat$3 or adjust$3 or compensat$3) with voltage$1 with oscillator$1 and ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:48	
20 BRS	2	measur$6 with (CPU or processor$1) with (consumption or power) and (calibrat$3 or adjust$3 or compensat$3) with voltage$1 with oscillator$1 and ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:49	
21 BRS	5	measur$6 with integrated with circuit$1 with power and (calibrat$3 or adjust$3 or compensat$3) with current$1 with voltage$2 and calculat$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:49	
22 BRS	0	measur$6 with (CPU or processor) with (consumption or power) and (calibrat$3 or adjust$3 or compensat$3) with voltage$1 with oscillator$1 with ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:51	
23 BRS	1	measur$6 with (CPU or processor) with (consumption or comsum$4 or power) and oscillat$3 with ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:51	

Type	Hits	Search Text	DBs	Time Stamp	Comments
24 BRS	1383	(calibrat$3 or adjust$3 or compensat$3) with current with source with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 10:13	
25 BRS	62	calibration with current with source with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:55	
26 BRS	0	calibration with current with source with draw$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:56	
27 BRS	0	measur$6 with (CPU or processor) with (consumption or power or consum$4) and calibration with current with source with draw$3 with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:56	
28 BRS	1	measur$6 with (CPU or processor) with (consumption or power or consum$4) and calibration with current with source with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:23	

Type	Hits	Search Text	DBs	Time Stamp	Comments
29 BRS	2	measur$6 with (CPU or processor) with (consumption or power or comsum$4) and (calibrat$3 or adjust$3 or compensat$3) with current with source with resistance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 12:58	
30 BRS	6	measur$6 with (CPU or processor) with (consumption or power or comsum$4) and (calibrat$3 or adjust$3 or compensat$3) with current with source with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:07	
31 BRS	1	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with (calibrat$3 or adjust$3 or compensat$3) with current with source with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:08	
32 BRS	1	measur$6 with (CPU or processor or circuit adj circuit$1) with (consumption or power or comsum$4) with (calibrat$3 or adjust$3 or compensat$3) with current with source with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:09	

Type	Hits	Search Text	DBs	Time Stamp	Comments
33 BRS	1	(measur$6 or comput$3 with calculat$3) with (CPU or processor or circuit adj circuit$1) with (consumption or power or comsum$4) with (calibrat$3 or adjust$3 or compensat$3) with current with source with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:21	
34 BRS	4	(measur$6 or comput$3 with calculat$3) with (CPU or processor or circuit adj circuit$1) with (consumption or power or comsum$4) with (calibrat$3 or adjust$3 or compensat$3) with (voltage$1 or current) with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:13	
35 BRS	0	(measur$6 or comput$3 with calculat$3) with (CPU or processor or circuit adj circuit$1) with (consumption or power or comsum$4) with inherent with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:14	
36 BRS	150	(measur$6 or comput$3 with calculat$3) with (CPU or processor or circuit adj circuit$1) with (consumption or comsum$4) with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:15	

Type	Hits	Search Text	DBs	Time Stamp	Comments
37	BRS	70	(measur$6 or comput$3 with calculat$3) with CPU with (consumption or power or comsum$4) with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:15	
38	BRS	0	(measur$6 or comput$3 with calculat$3) with CPU with (consumption or power or comsum$4) with resist$4 with ladder	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:16	
39	BRS	0	(measur$6 or comput$3 with calculat$3) with (CPU or processor or circuit adj circuit$1) with (consumption or power or comsum$4) with resist$4 with ladder$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:15	
40	BRS	6	(measur$6 or comput$3 with calculat$3) with CPU with (consumption or power or comsum$4) and resist$4 with ladder	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 13:16	
41	BRS	2	(calibrat$3 or adjust$3 or compensat$3) with ammeter$1 with cpu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:21	

Type	Hits	Search Text	DBs	Time Stamp	Comments
42 BRS	60	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with resistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:24	
43 BRS	0	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with cross with resistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:24	
44 BRS	3	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with drop$3 with resistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:25	
45 BRS	2	measur$6 with integrated with circuit$1 with (CPU or processor) with (consumption or power or comsum$4) with resistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:26	
46 BRS	2	measur$6 with integrated with circuit$1 with (CPU or processor) with (consumption or power or comsum$4) with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:27	
47 BRS	25	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with resist$4 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:27	

Type	Hits	Search Text	DBs	Time Stamp	Comments
48 BRS	26	measur$6 with (CPU or processor) with (consumption or power or comsum$4) and precision with resist$4 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:27	
49 BRS	0	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with precision with resist$4 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:27	
50 BRS	0	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with precision with resist$4 and integrated with circuit$1 and (current$1 or voltage$1) with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:28	
51 BRS	9	measur$6 with (CPU or processor) with (consumption or power or comsum$4) and precision with resist$4 and integrated with circuit$1 and (current$1 or voltage$1) with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:29	

Type	Hits	Search Text	DBs	Time Stamp	Comments
52	BRS	0	measur$6 with (CPU or processor) with (consumption or power or comsum$4) and precision with resist$4 and integrated with circuit$1 and (calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:29	
53	BRS	1	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with resist$4 and integrated with circuit$1 and (calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:30	
54	BRS	1	measur$6 with (CPU or processor) with (consumption or power or comsum$4) with resist$4 and integrated with circuit$1 and (calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:30	
55	BRS	0	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and resist$4 with ladder with voltage with taps	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:27	

Type	Hits	Search Text	DBs	Time Stamp	Comments
56	BRS	3	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and resist$4 with ladder$1 with (voltage$1 or current$1) with tap$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:40	
57	BRS	2	interleav$3 with (calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 08:12	
58	BRS	2	"20040030532"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:55	
59	BRS	2	"200040102906"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 16:56	
60	BRS	5	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 with ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:30	
61	BRS	227	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 with integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:30	

Type	Hits	Search Text	DBs	Time Stamp	Comments
62 BRS	0	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 with integrated with circuit$1 and ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:31	
63 BRS	2	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 same integrated with circuit$1 and ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:34	
64 BRS	2	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 same (cpu or processor or integrated with circuit$1) and ammeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:35	
65 BRS	401	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 with (cpu or processor or integrated with circuit$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:35	
66 BRS	3	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 with (cpu or processor) with integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:36	

Type	Hits	Search Text	DBs	Time Stamp	Comments
67	BRS	9	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and ammeter$1 and (calculat$3 or measur$3) with (current$1 or voltage$1) with (power or consumption or consum$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:39	
68	BRS	1	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and ammeter$1 and (calculat$3 or measur$3) with (current$1 or voltage$1) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:40	
69	BRS	2	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and ammeter$1 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:42	

Type	Hits	Search Text	DBs	Time Stamp	Comments
70 BRS	68	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:43	
71 BRS	4	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1) with resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:44	
72 BRS	45	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1) and resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:08	
73 BRS	0	ammeter$1 with cpu with die	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:55	

Type	Hits	Search Text	DBs	Time Stamp	Comments
74 BRS	21	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1or micro-controller) and resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:57	
75 BRS	46	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1 or micro-controller) and resist$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:58	
76 BRS	0	micro-controller$1 with measur$6 with voltage with drop$1 with resist$4 with power with level$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:59	
77 BRS	1	controller$1 with measur$6 with voltage with drop$1 with resist$4 with power with level$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 08:59	

Type	Hits	Search Text	DBs	Time Stamp	Comments
78 BRS	0	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1) and resist$4 with temperature41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:01	
79 BRS	2	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1) and micro-controller$1) and resist$4 with temperature$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:03	
80 BRS	48	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1 and micro-controller$1) and resist$4 with temperature$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:23	

Type	Hits	Search Text	DBs	Time Stamp	Comments
81 BRS	1	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1 and micro-controller$1) and resist$4 with temperature$1 and (ammeter$1 or voltmeter$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:04	
82 BRS	14	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1) and resist$4 and temperature$1 and voltage with drop	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:01	
83 BRS	1677	702/57,60,64,65,69.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:35	
84 BRS	2041	702/57,60,64,65,69,124.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:36	

Type	Hits	Search Text	DBs	Time Stamp	Comments
85 BRS	309	324/130.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:38	
86 BRS	537	327/554,558.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:41	
87 BRS	3934	318/685,696.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:41	
88 BRS	292	331/44.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:55	
89 BRS	615	331/44,46.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:00	

Type	Hits	Search Text	DBs	Time Stamp	Comments
90 BRS	39	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1) and resist$4 and temperature$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 09:58	
91 BRS	6374	331/44,46,47,48,65,66,116r,158,17 6,177r,177v.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:47	
92 BRS	1	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and (calculat$3 or measur$3) with (power or consumption or consum$4) with (cpu or processor$1 or microprocessor$1) and resist$4 and temperature$1 and voltage with drop and (ammeter$1 or voltmeter$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:03	
93 BRS	460	ammeter$1 adj voltmeter$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:45	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
94	BRS	13	select$3 with resist$4 with ladder$1 with voltage$1 with taps	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:18	
95	BRS	65	resist$4 with ladder with voltage with taps	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:21	
96	BRS	0	(calibrat$3 or adjust$3 or compensat$3) with (current$1 or voltage$1) with oscillat$3 and select$3 with resist$4 with ladder$1 with voltage$1 with taps	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:24	
97	BRS	0	(calibrat$3 or adjust$3 or compensat$3 or tun$4) with (current$1 or voltage$1) with oscillat$3 and select$3 with resist$4 with ladder$1 with voltage$1 with taps	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:24	
98	BRS	0	(calibrat$3 or adjust$3 or compensat$3 or tun$4) with (current$1 or voltage$1) with oscillat$3 and resist$4 with ladder$1 with voltage$1 with taps	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:25	

Type		Hits	Search Text	DBs	Time Stamp	Comments
99	BRS	5	(calibrat$3 or adjust$3 or compensat$3 or tun$4) with (current$1 or voltage$1) with oscillat$3 and resist$4 with ladder$1 with voltage$1 with tap$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:42	
100	BRS	0	(calibrat$3 or adjust$3 or compensat$3 or tun$4) with (current$1 or voltage$1) with oscillat$3 same resist$4 with ladder$1 with voltage$1 with tap$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:38	
101	BRS	0	(calibrat$3 or adjust$3 or compensat$3 or tun$4) with (current$1 or voltage$1) with oscillat$3 same resist$4 with ladder$1 with tap$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:27	
102	BRS	0	ammeter$1 adj voltmeter$1 adj current adj sensor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:45	
103	BRS	7	ammeter$1 adj current adj sensor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 10:45	

Type	Hits	Search Text	DBs	Time Stamp	Comments
104 BRS	0	(calibrat$3 or adjust$3 or compensat$3 or tun$4) with (voc$1 or ((current$1 or voltage$1) with oscillat$3)) same resist$4 with ladder$1 with voltage$1 with tap$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:38	
105 BRS	0	(calibrat$3 or adjust$3 or compensat$3 or tun$4) with (current$1 or voltage$1) with oscillat$3 same resist$4 with ladder$1 with voltage$1 with tap$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:42	
106 BRS	3	resist$4 with ladder with voltage with taps with select$3 and count$3 with (period$1 or interval or duration$1 or time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:51	
107 BRS	0	resist$4 adj ladder with voltage adj tap$1 with voc$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:52	
108 BRS	0	resist$4 adj ladder with voltage adj tap$1 with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:52	

Type	Hits	Search Text	DBs	Time Stamp	Comments
109 BRS	2	resist$4 adj ladder with voltage adj tap$1 and oscillator$1	US-PGPUB; USPAT; EPO; IBM_TDB	2004/12/10 11:52	
110 BRS	21	resist$4 adj ladder with voltage adj tap$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:59	
111 BRS	0	resist$4 with ladder with voltage with tap$1 with oscillator$1 with voltage$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:54	
112 BRS	0	resist$4 with ladder with voltage with tap$1 with voc$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:55	
113 BRS	0	resist$4 with ladder with voltage with tap$1 with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:55	
114 BRS	0	resist$4 with ladder with voltage$1 with tap$1 with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:55	

Type	Hits	Search Text	DBs	Time Stamp	Comments
115 BRS	0	resist$4 with ladder with tap$1 with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:56	
116 BRS	0	resist$4 with ladder$1 with tap$1 with oscillator$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:56	
117 BRS	41	resist$4 with ladder with voltage with tap$1 and oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 11:59	
118 BRS	47	resist$4 with ladder$1 with voltage$1 with tap$1 and oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 08:20	
119 BRS	1	resist$4 with ladder$1 with voltage$1 with tap$1 same oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:00	
120 BRS	2	resist$4 with ladder$1 with voltage$1 with tap$1 and oscillat$3 and controller$1 with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:10	

Type	Hits	Search Text	DBs	Time Stamp	Comments
121 BRS	7	resist$4 with ladder$1 with voltage$1 with tap$1 and oscillat$3 and (microprocessor$1 or CPU or processor$1 or micro-controller$1 or controller$1) with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:13	
122 BRS	5	resist$4 with ladder$1 with voltage$1 with tap$1 and (calibrat$3 or adjust$3 or compensat$3 or tun$4) with oscillat$3 and (microprocessor$1 or CPU or processor$1 or micro-controller$1 or controller$1) with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:15	
123 BRS	5	resist$4 with ladder$1 with voltage$1 with tap$1 and (calibrat$3 or adjust$3 or compensat$3 or tun$4) with measur$6 and oscillat$3 and (microprocessor$1 or CPU or processor$1 or micro-controller$1 or controller$1) with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:37	

Type	Hits	Search Text	DBs	Time Stamp	Comments
124 BRS	1	resist$4 with ladder$1 with voltage$1 with tap$1 and oscillat$3 with (microprocessor$1 or CPU or processor$1 or micro-controller$1 or controller$1) with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:35	
125 BRS	11	resist$4 with ladder$1 with voltage$1 with tap$1 and oscillat$3 with count$4 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:35	
126 BRS	0	resist$4 with ladder$1 with voltage$1 with tap$1 and (calibrat$3 or adjust$3 or compensat$3 or tun$4) with oscillat$3 with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:37	
127 BRS	11	resist$4 with ladder$1 with voltage$1 with tap$1 and (calibrat$3 or adjust$3 or compensat$3 or tun$4) and oscillat$3 with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:38	
128 BRS	10	resist$4 with ladder$1 with voltage$1 with tap$1 and (calibrat$3 or adjust$3 or compensat$3 or tun$4) and oscillat$3 with count$4 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:39	

Type	Hits	Search Text	DBs	Time Stamp	Comments
129 BRS	2	resist$4 with ladder$1 with voltage$1 with tap$1 and (calibrat$3 or adjust$3 or compensat$3 or tun$4) and oscillat$3 with voltage$1 and frequenc$3 with count$4 and integrated with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:43	
130 BRS	6480	331/15,44,46,47,48,65,66,116r,158,176,177r,177v.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:47	
131 BRS	7	resist$4 with ladder$1 with voltage$1 with tap$1 and oscillat$3 and (microprocessor$1 or CPU or processor$1 or micro-controller$1 or controller$1) with count$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/10 12:52	
132 BRS	0	interleav$3 with (calibrat$3 or adjust$3 or compensat$3 or tun$3) with oscillat$3 with measur$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 08:11	
133 BRS	14	interleav$3 with (calibrat$3 or adjust$3 or compensat$3) with oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 08:12	

Type	Hits	Search Text	DBs	Time Stamp	Comments
134 BRS	9	resist$4 with ladder$1 with voltage$1 with tap$1 with space$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 08:55	
135 BRS	0	resist$4 with ladder$1 with tap$1 with event$2 and oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 08:32	
136 BRS	1	resist$4 with ladder$1 with tap$1 with even$2 and oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 10:46	
137 BRS	1	resist$4 with ladder$1 with tap$1 with space$1 and oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 08:34	
138 BRS	19	"702"/$.ccls. and raymond.xp.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 09:04	
139 BRS	1	"702"/$.ccls. and tsai.xp.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 09:04	

Type	Hits	Search Text	DBs	Time Stamp	Comments
140 BRS	10	vco$1 with (CPU or processor$1) with (die$1 or (integrated adj circuit$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 09:27	
141 IS&R	2	("4623838").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 10:02	
142 BRS	914	(calibrat$3 or adjust$3 or compensat$3 or tun$3) with (tim$3 or period$1 or interval$1 or duration$1) with variation$1 with temperature$1 and repeat$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 10:15	
143 BRS	9	(calibrat$3 or adjust$3 or compensat$3 or tun$3) with (tim$3 or period$1 or interval$1 or duration$1) with variation$1 with temperature$1 with repeat$4 and (voc$1 or oscillat$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 10:16	
144 BRS	34	(calibrat$3 or adjust$3 or compensat$3 or tun$3) with (voc$1 or oscillat$3)with (tim$3 or period$1 or interval$1 or duration$1) with variation$1 with temperature$1 and repeat$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/11 10:17	